

① In the following circuit  $|V_L| = 240 \text{ V (RMS)}$  at all times. The following information about the loads are given =

$d_1$ : absorbs 180 Watts and 240 VARs.

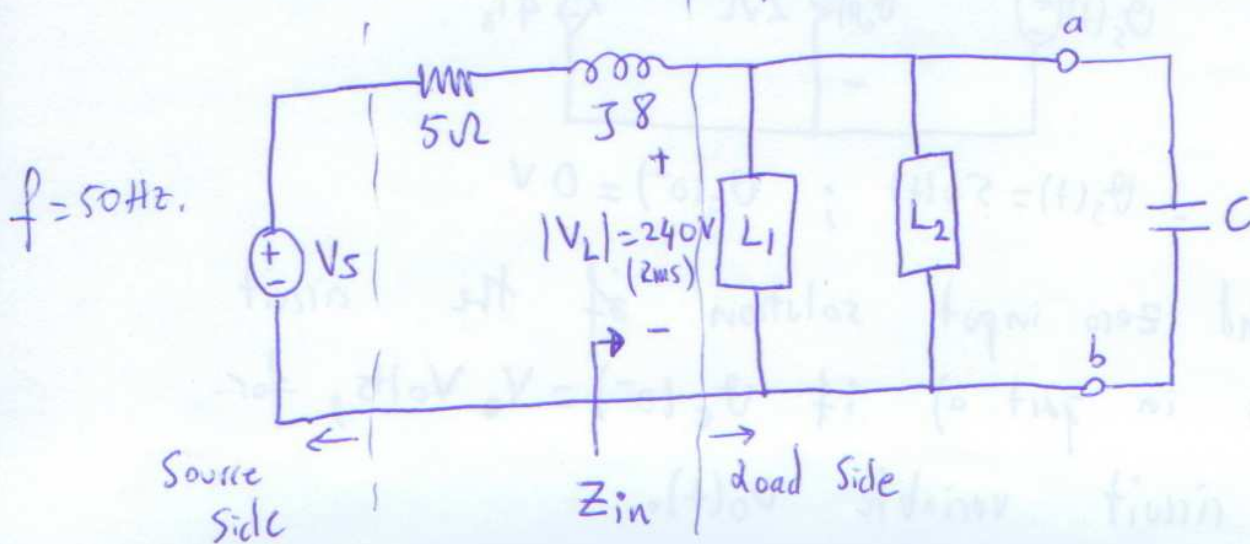
$d_2$ : absorbs 600 VA at 0.6 pf lagging

a) Assume there is no capacitor connected between a-b terminals. Find input impedance at the load side, source voltage  $|V_S|$  (in RMS) and pf. on source side.

b) Now, the capacitor is connected between a-b terminals. Find value of C such that average power absorbed by 5 ohm resistor is minimum. Find  $|V_S|$  with the compensation capacitor.

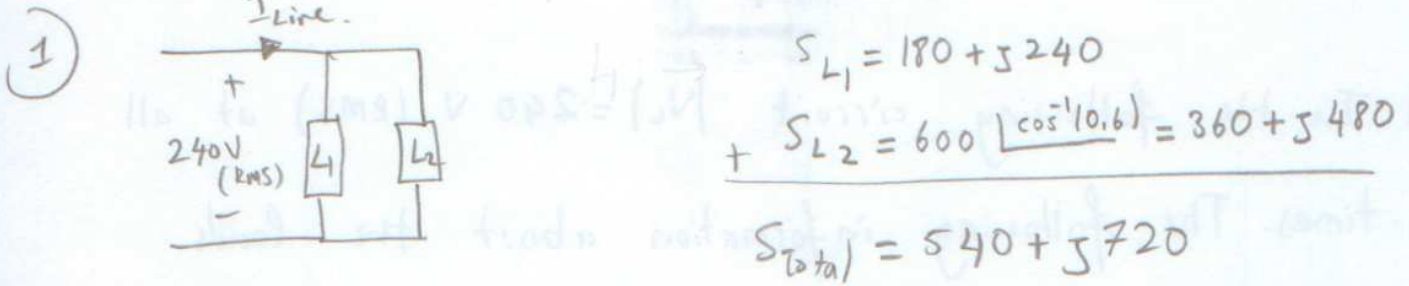
c) Find value of C such that load side pf. is 0.9 lagging.

Find  $|V_S|$  with compensation capacitor.



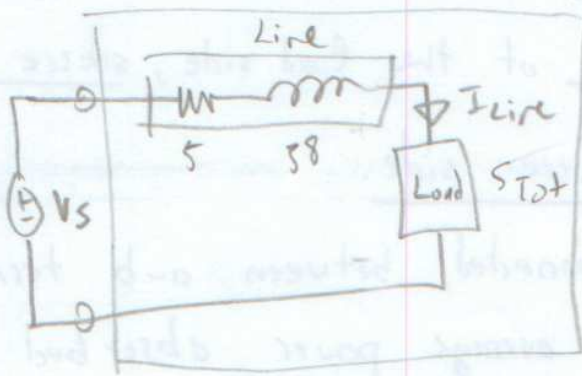
# Exam 4 Solutions

①



a)  $|I_{Line}| = \frac{|S_{Total}|}{240} = \frac{900}{240} = 3.75 \text{ A (RMS)}$

$$S_{Total} = |I_{Line}|^2 Z \rightarrow Z = \frac{540 + j720}{(3.75)^2} = 38.4 + j51.2 = 64 \angle 53.1^\circ$$



$$S_{Line} = |I_{Line}|^2 (5 + j38) = 70.3125 + j112.5$$

$$S_{source} = S_{Line} + S_{Total} = 610.31 + j832.5$$

$$|S_{source}| = |I_{Line}| \cdot |V_s| \rightarrow |V_s| = \frac{|610.31 + j832.5|}{3.75} = 275.26 \text{ V RMS}$$

p.f on source side:  $\cos(\tan^{-1} \frac{832.5}{610.31}) = 0.59$  lagging.

b)  $S_{before} = 540 + j720$

$$I_{Line}^{before} = 3.75 \text{ A}$$

$$S_{after} = 540 + j720 - j720 = 540$$

$$I_{Line}^{after} = \frac{540}{240} = 2.25 \text{ A}$$

↑ compensation

$$S_{Line} = (2.25)^2 (5 + j38) = 25.31 + j40.5$$

$$S_{source} = 540 + 25.31 + j40.5 \Rightarrow$$



After -

$$S_{\text{source}} = 565.31 + j40.5 \quad (2)$$

$$|S_{\text{source}}| = 566.75 = |V_S| \cdot |I_{\text{line}}| \rightarrow$$

$$\rightarrow |V_S| = 251.89 \text{ V (RMS)}$$

$$S_{\text{compensator}} = -j720$$

$$S_{\text{compensator}} = \frac{|V_{\text{cap}}|^2}{X_C^*}$$

240

$$X_C = \frac{|V_{\text{cap}}|^2}{S_{\text{comp}}^*} = \frac{(240)^2}{j720}$$

$$= -j380$$

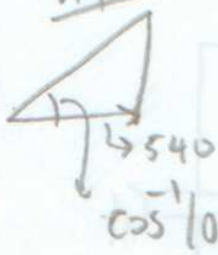
$$X_C = \frac{-j}{\omega C}$$

$$X_C = -j380 \rightarrow C = \frac{1}{(2\pi \cdot 50) \cdot 380} = 39.8 \mu\text{F}$$

In this problem, to minimize power loss on line resistor, the current through the resistor should be minimized. Therefore

load side p.f. should be 1.0 after compensation to minimize loss)

c) After



$$\cos^{-1}(0.9) = 25.84$$

$$S^{\text{After}} = 540 + j \tan(25.84^\circ) 540$$

$$= 540 + j261$$

$$S^{\text{After}} = S^{\text{before}} + S^{\text{compensation}}$$

$$S^{\text{compensation}} = -j458.5$$

$$X_C = -j \frac{(240)^2}{458.5} = -j125.63; C = \frac{1}{100\pi \cdot 125.6} = 25.3 \mu\text{F}$$

$$|I_{Line}|^{after} = \frac{|S^{after}|}{240} = \frac{540/0.9}{240} = 2.5 A. \quad (3)$$

$$S^{line} = (2.5)^2 (5 + j8) = 31.25 + j50 \quad (1)$$

$$S^{source} = 571.25 + j301 \quad (\text{supplied})$$

$$|S^{source}| = |V_s| \cdot |I_{Line}| \rightarrow \frac{V_s = 258.27 V}{(pm)}$$

$\uparrow$  645       $\uparrow$  2.5

(d) Assume there is no capacitor connected between a-b terminals. Find input impedance at the load side, source voltage

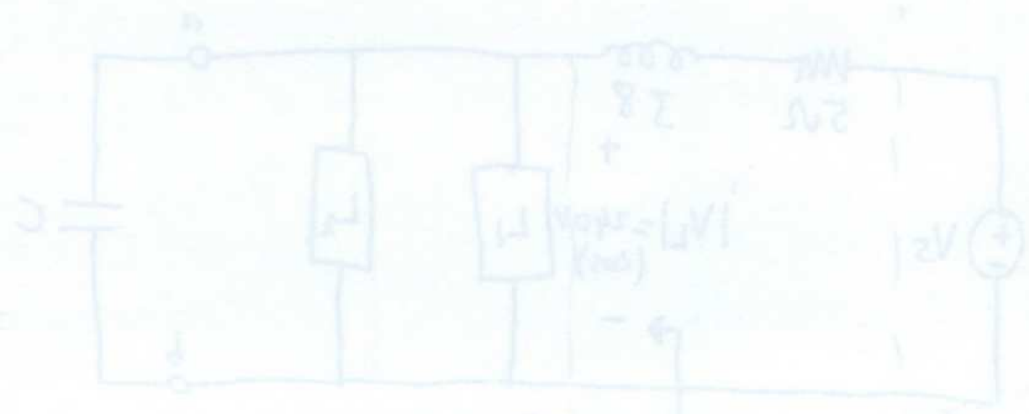
$|V_2|$  (rms) and  $P_{in}$  (watts) at source side.

(d) Now, the capacitor is connected between a-b terminals. Find value of C such that average power absorbed by

load resistor is minimum. Find  $|V_2|$  with the compensation capacitor.

(e) Find value of C such that load side  $P_{in}$  is 0W.

Find  $|V_2|$  with compensation capacitor.



Source voltage 258.27 V  
Load resistor 28 ohms